

FIG. 1 is a block diagram of a system architecture. The system includes a Motherboard Section (10), a System Connection Back Plane (20), a System Management Unit (30), and Peripheral Devices (40). A Switch Control Signal (50) is provided to the System Connection Back Plane (20) and the System Management Unit (30).

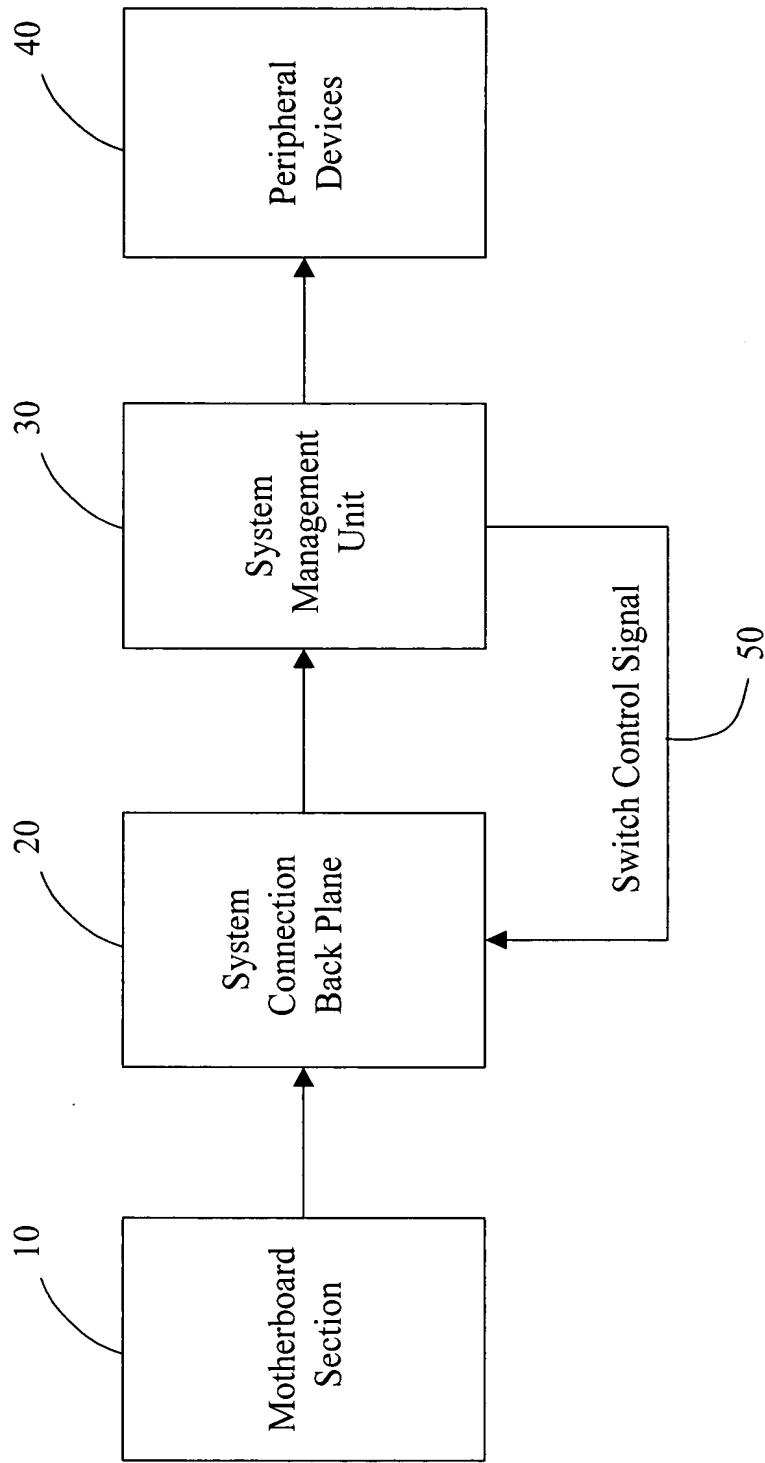


FIG.1

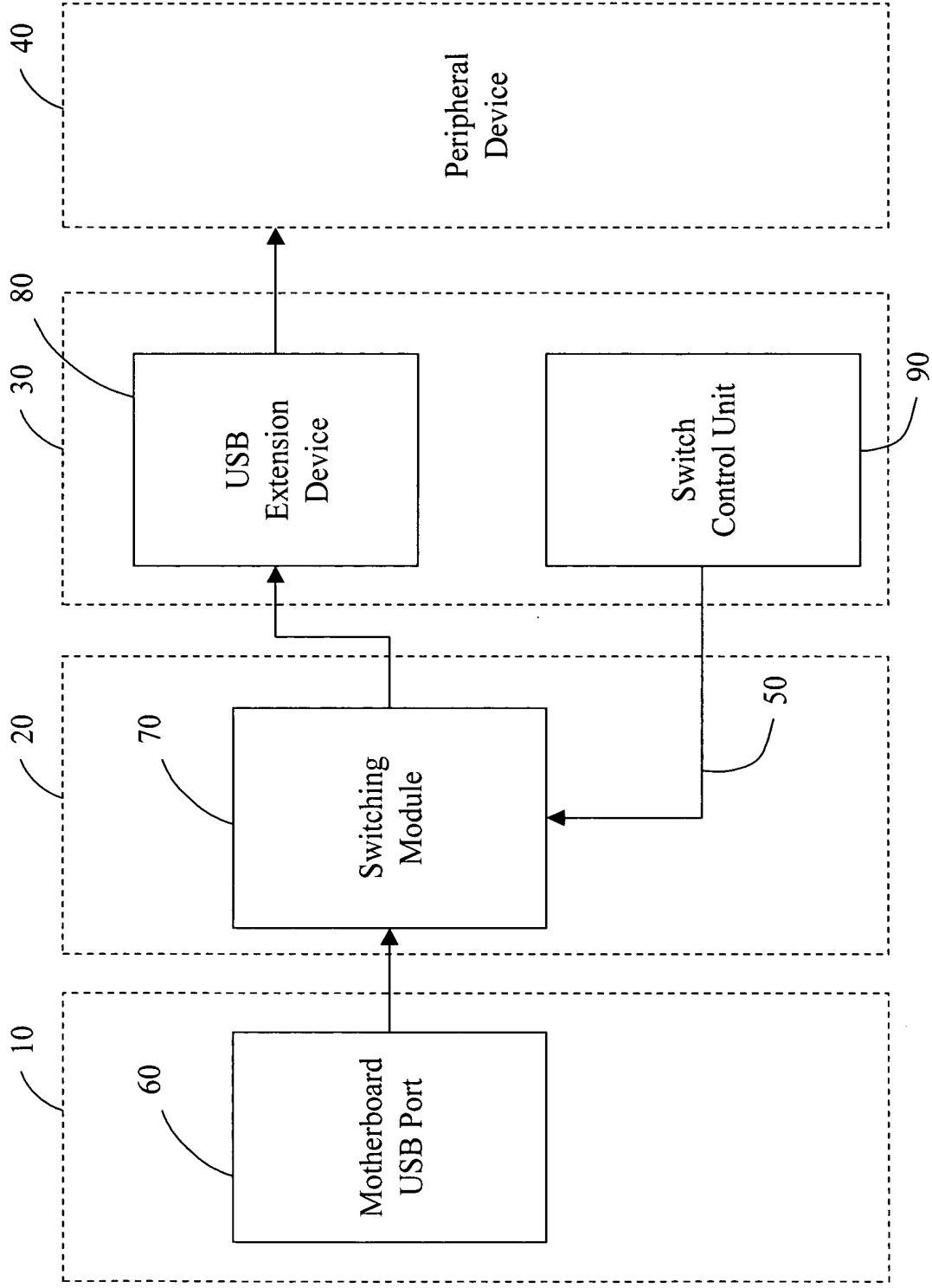


FIG. 2

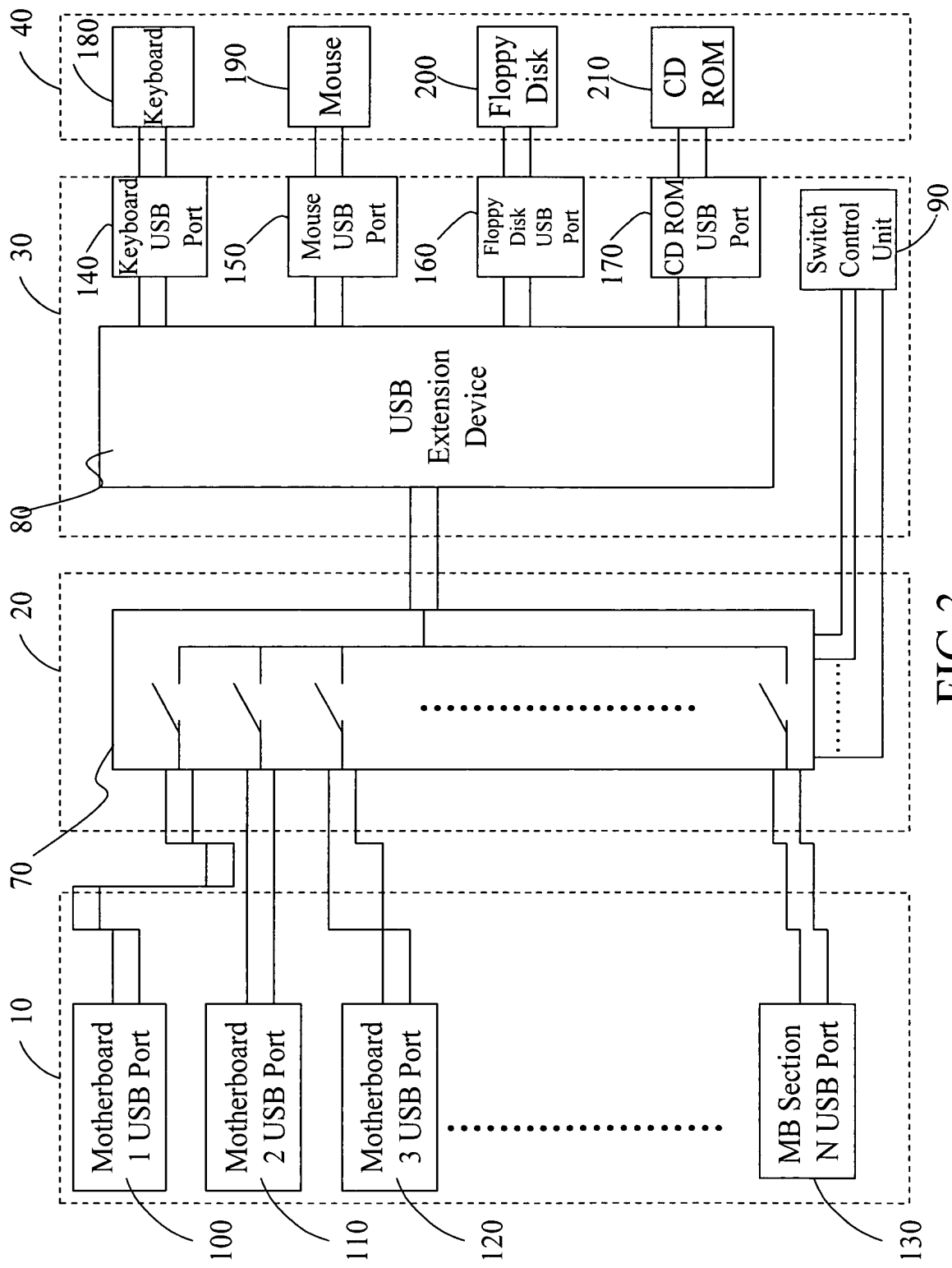


FIG.3